

APPLICANT'S ART CITATION

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Application Not Assigned

OFGS File No. P/10-582

Applicant NEWMAN, Jr., et al.

Filing Date Filed Herewith

Group Art Unit

11046 U.S. PTO
10/006036
12/05/01

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number							Date	Name	Class	Sub-class	Filing Date If Appropriate
MA	5	6	0	8	2	9	2	3/4/1997	KONOPKA et al.	315	2018	
MA	4	6	6	3	5	7	0	5/5/1987	LUCHACO et al.	315	219	
MA	5	6	9	4	0	0	6	12/2/1997	KONOPKA	315	219	
MA	5	8	7	2	4	3	0	2/16/1999	KONOPKA	315	219	
MA	5	5	1	7	0	8	6	5/14/1996	EL-HAMAMSY et al.	315	247	
MA	5	3	9	9	9	4	4	3/21/1995	KONOPKA et al.	315	219	
MA	5	9	9	4	8	4	7	11/30/1999	KONOPKA	315	2098	
MA	5	3	8	7	8	4	7	2/7/1995	WOOD	315	2098	
MA	5	8	0	4	9	2	6	9/8/1998	DEAVENPORT	315	219	
MA	6	1	4	1	2	3	0	10/31/2000	SUM	363	61	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

MA	"Fluorescent Ballast Design Using Passive P.F.C. and Crest Factor Control" by Peter N. Wood, International Rectifier Corporation 1998.
MA	"Single-Switch Frequency-Controlled Electronic Dimming Ballast with Unity Power Factor", Chang-Shiarn Lin et al., IEEE Transactions on Aerospace and Electronic Systems, Vol. 36, No. 3, pages 1001-1006, July, 2000.
MA	"Single-Stage High Power-Factor Electronic Ballast" M. Marx et al., PEMC
MA	"Ripple-Free, Single-Stage Electronic Ballasts with Dither-Booster Power Factor Corrector" T.F. Wu et al., IEEE Industry Applications Society, pages 2372-2379, January, 1997.
MA	"High Power Factor Correction Circuit Using Valley Charge-Pumping for Low Cost Electronic Ballasts" G. Chae et al., IEEE, pages 2003-2008, August, 1998.
MA	"A Unity Power Factor Electronic Ballast for Fluorescent Lamp Having Improved Valley Fill and Valley Boost Converter", Y. Youn et al., PESC97 Record IEEE, pages 53-59, May, 1997.

Examiner

Chah MA

Date Considered

11/29/02

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